

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANTS**

OCT 07 2006  
PTO-1449

Attorney Docket No.  
**2885/56**

Serial No.  
**10/009,649**

Applicant(s)  
**Vorbach et al.**

Filing Date  
**May 29, 2002**

Group Art Unit  
**2192**

PATENT & TRADEMARK OFFICE  
U.S. DEPARTMENT OF COMMERCE

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT/PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
TD	4,498,134	Feb 1985	Hansen et al.			
	4,720,778	Jan 1988	Hall et al.			
	5,600,845	Feb 1997	Gilson, Kent L.			
	5,737,516	Apr 1998	Circello et al.			
	5,841,973	Nov 1998	Cooke et al.			
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	5,960,200	Sep 1999	Eager et al.			
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	6,003,143	Dec 1999	Kim et al.			
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	6,150,839	Nov 2000	New et al.			
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	6,212,650	Apr 2001	Guccione, Steven A.			
	6,216,223	Apr 2001	Revilla et al.			
	6,240,502	May 2001	Panwar et al.			
	6,282,701	Aug 2001	Wygodny et al.			
	6,286,134	Sep 2001	Click, Jr. et al.			
	6,301,706	Oct 2001	Maslennikov et al.			
	6,398,383	Jun 2002	Huang, Yu-Hwei			
	6,404,224	Jun 2002	Azegami et al.			
	6,421,809	Jul 2002	Wuytack et al.			
	6,434,695	Aug 2002	Esfahani et al.			
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	6,496,971	Dec 2002	Lesea et al.			
	6,542,844	Apr 2003	Hanna, Stephen Dale			
	6,704,816	Mar 2004	Burke, David			
	6,717,436	Apr 2004	Kress et al.			
	6,757,847	Jun 2004	Farkash et al.			
	6,782,445	Aug 2004	Olgiati et al.			
	6,785,826	Aug 2004	Durham et al.			
	6,961,924	Nov 2005	Bates et al.			
	US 2003/0192032	Oct 2003	Andrade et al.			
	US 2003/0061542	Mar 2003	Bates et al.			
	US 2003/0192032	Oct 2003	Andrade et al.			

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O I P E NCT n 2005 88 REMARKS	6,301,706	Oct 2001	Maslennikov et al.			
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	6,404,224	Jun 2002	Azegami et al.			
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↓	US 2003/0192032	Oct 2003	Andrade et al.			
↓	US 2003/0061542	Mar 2003	Bates et al.			

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↓	0 485 690	Jan 2, 1992	EPO				
↓	0 497 029	Aug 5, 1992	EPO				
↓	WO 99/00731	Jan 7, 1999	WIPO				
↓	WO 99/40522	Aug 12, 1999	WIPO				

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TD	Beck et al., From control flow to data flow, TR 89-1050, October 1989, Dept. of Computer Science, Cornell University, Ithaca, NY, pp. 1-25.
↓	Hauser, J.R. et al., "GARP: A MIPS Processor With A Configurable Coprocessor," University of California, Berkeley, IEEE, 1997, pp. 24-33.
↓	Hedge, S.J. et al., "3-D WASP Devices for On-Line Signal and Data Processing," Aspex Microsystems, Brunel University, Uxbridge, United Kingdom, IEEE 1994, pp. 11-21.
↓	Iseli, C., et al., "A C++ Compiler for FPGA Custom Execution Units Synthesis," IEEE, 1995, pp. 173-179.
↓	Jacob, Jeffrey et al., "Memory Interfacing and Instructions Specification for Reconfigurable Processors," ACM 1999, pp. 145-154.
↓	Ling, X., "WASMII: An MPLD with Data-Driven Control on a Virtual Hardware," Journal of Supercomputing, Kluwer Academic Publishers, Dordrecht, Netherlands, 1995, pp. 253-276.
↓	Lizy, J., et al., "A Dynamically Reconfigurable Interconnect for Array Processors," IEEE March 1998, Vol. 6, No. 1, pp. 150-157.

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TD O I P E DCT 0 2 2006 & REMOVED	Villasenor, J. et al., "Express Letters Video Communications Using Rapidly Reconfigurable Hardeare," IEEE Transactions on Circuits and Systems for Video Technology, New York, December 1995, pp. 565-567. Ye, Z.A. et al., "A Compiler for A Processor with A Reconfigurable Functional Unit," FPGA 2000, ACM/SIGNA International Symposium on Field Programmable Gate Arrays, Monterey, CA, Feb. 9-11, 2000, pp. 95-100. Weinhardt, Markus et al., "Pipeline Vectorization for Reconfigurable Systems," 1999, IEEE, pp. 52-62. Weinhardt, Markus et al., "Pipeline Vectorization," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 20, No. 2, February 2001, pp. 234-248. Xu, Hongyu et al., "Parallel QR Factorization on a Block Data Flow Architecture," Department of ECE, North Carolina State University, IEEE , 1992, pp. 332-336.
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TD	6,519,674	February 11, 2003	Lam et al.			
	6,526,520	February 25, 2003	Vorbach et al.			
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TD	2004/0025005	February 5, 2004	Vorbach et al.			

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EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO
TD 12/7/06	0 221 360	May 13, 1987	Europe				
TD	0 428 327	May 22, 1991	Europe				
	0 477 809	April 1, 1992	Europe				
	0 539 595	May 5, 1993	Europe				
	0 628 917	December 14, 1994	Europe				
	0 678 985	October 25 1995	Europe				
	0 686 915	December 13, 1995	Europe				
	0 707 269	April 17 1996	Europe				
TD	0 726 532	July 2, 1998	Europe				

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10/009649  
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	<b>APPLICANT</b> <b>Vorbach, et al.</b>	
	<b>FILING DATE</b> <b>herewith</b>	<b>GROUP 2192</b> <b>Not yet assigned</b>

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\*Copy of the reference is not provided (reference cited in Search Report).

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EXAMINER INITIAL		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
TD		Ferreira J et al: "The program dependence graph and its use in optimization" ACM Transactions on Programming Languages and Systems, July 1987, USA, [online] Bd. 9, Nr. 3, Seiten 319-349, XP002156651 ISSN: 0164-0935 ACM Digital Library*
TD		HWANG L. J et al: "Min-cut replication in partitioned networks" IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, [online] Bd. 14, Nr. 1, January 1995, Seiten 96-106, XP000532228 USA ISSN: 0278-0070 IEEE Xplore*

EXAMINER	Thuy Dao	DATE CONSIDERED and	3/10/06 12/10/06
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